

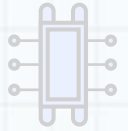
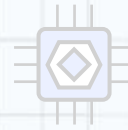
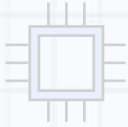


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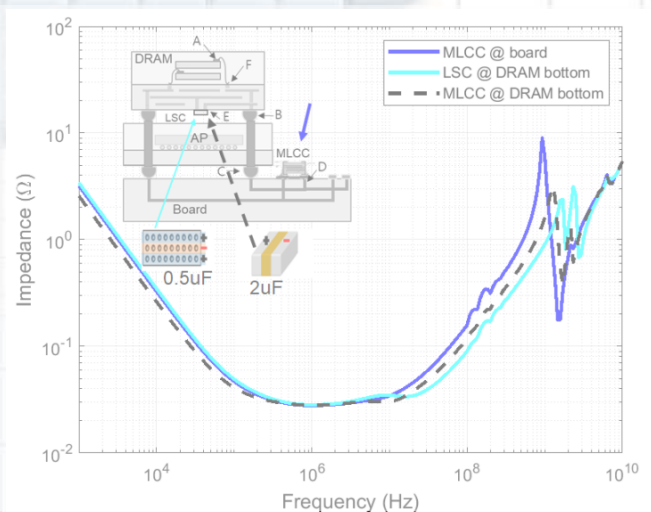
Simulation-Based Analysis of Land-Side Capacitor Integration in LPDDR5 DRAM



Friday 16 May 2025

When: 10:30am

Where: B0.6



Signal Integrity (Prof. A. Orlandi, F. de Paulis)

Laboratory of Signal Integrity (Prof. F. de Paulis)